

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions and listing of claims in the application:

Listing of Claims:

1-22. (canceled).

23. (currently amended). A display device, comprising:

a display panel having an electrode; and

a driver that drives said electrode of said display panel, said driver comprising:

a first switching element that supplies a charge from a recovering capacitor to said electrode of said display panel;

an interconnector connected to said first switching element through a first one-way conductive element;

a second switching element that recovers the charge from said electrode of said display panel to said recovering capacitor;

a second one-way conductive element provided between said second switching element and said interconnector; and

a frequency reducer connected in parallel with said first switching element that is operable to reduce a resonance frequency of an LC resonance resulting from a parasitic capacitance of said first switching element and an inductance component of said interconnector, wherein the charge is supplied to said electrode of said display panel from said recovering

capacitor through said first switching element and said interconnector, wherein said frequency reducer has a capacitance of approximately five to ten times as much as that of the parasitic capacitance of said first switching element, to suppress unwanted electromagnetic wave radiation of 30 MHz or higher.

24. (currently amended). A display device, comprising:

a display panel having an electrode; and

a driver that drives said electrode of said display panel, said driver comprising:

a first switching element that supplies a charge from a recovering capacitor to said electrode of said display panel;

an interconnector connected to said first switching element through a first one-way conductive element;

a second switching element that recovers the charge from said electrode of said display panel to said recovering capacitor;

a second one-way conductive element provided between said second switching element and said interconnector; and

a frequency reducer connected in parallel with said second switching element that is operable to reduce a resonance frequency of an LC resonance resulting from a parasitic capacitance of said second switching element and an inductance component of said interconnector, wherein the charge is recovered to said recovering capacitor from said electrode of said display panel through said second switching element and said interconnector, wherein said frequency reducer has a capacitance of approximately five to ten times as much as that of

the parasitic capacitance of said second switching element, to suppress unwanted electromagnetic wave radiation of 30 MHz or higher.

25. (currently amended). A display device, comprising:

a display panel having an electrode; and

a driver that drives said electrode of said display panel, said driver comprising:

a first switching element that supplies a charge from a recovering capacitor to said electrode of said display panel;

an interconnector connected to said first switching element through a first one-way conductive element;

a second switching element that recovers the charge from said electrode of said display panel to said recovering capacitor;

a second one-way conductive element provided between said second switching element and said interconnector;

a first frequency reducer connected in parallel with said first switching element that is operable to reduce a resonance frequency of an LC resonance resulting from a parasitic capacitance of said first switching element and an inductance component of said interconnector, wherein said first frequency reducer has a capacitance of approximately five to ten times as much as that of the parasitic capacitance of said first switching element, to suppress unwanted electromagnetic wave radiation of 30 MHz or higher; and

a second frequency reducer connected in parallel with said second switching element that is operable to reduce a resonance frequency of an LC resonance resulting from a parasitic capacitance of said second switching element and an inductance component of said

interconnector, wherein the charge is supplied to said electrode of said display panel from said recovering capacitor through said first switching element and said interconnector, and the charge is recovered to said recovering capacitor from said electrode of said display panel through said second switching element and said interconnector, wherein said second frequency reducer has a capacitance of approximately five to ten times as much as that of the parasitic capacitance of said second switching element, to suppress unwanted electromagnetic wave radiation of 30 MHz or higher.

26. (New). A plasma display device, comprising:

a plasma display panel;

a recovering capacitor that supplies a charge to a capacitive load of said plasma display panel;

a first switching element connected to a first voltage source;

an interconnector connected between said first switching element and said plasma display panel;

a first capacitive device that connects said first voltage source to said interconnector in parallel with said first switching element;

a second switching element connected to a second voltage source;

a second capacitive device that connects said second voltage source to said interconnector in parallel with said second switching element;

a third switching element connected to said recovering capacitor; and

an inductance element connected between said third switching element and said interconnector,

wherein when said third switching element is turned on, a potential of said interconnector rises and starts to fall from a peak voltage, and thereafter said first switching element is turned on, so that the potential of said interconnector becomes equal to a potential of said first voltage source.

27. (New). A plasma display device, comprising:

a plasma display panel;

a recovering capacitor that supplies a charge to a capacitive load of said plasma display panel;

a first transistor connected to a first voltage source;

an interconnector connected between said first transistor and said plasma display panel;

a first capacitive element connected between a drain and a source of said first transistor;

a second transistor connected to a second voltage source;

a second capacitive element connected between a drain and a source of said second transistor;

a third transistor connected to said recovering capacitor; and

an inductance element connected between said third transistor and said interconnector,

wherein when said third switching element is turned on, a potential of said interconnector rises and starts to fall from a peak voltage, and thereafter said first switching element is turned on, so that the potential of said interconnector becomes equal to a potential of said first voltage source.